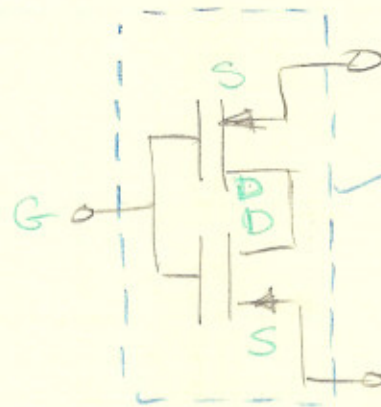


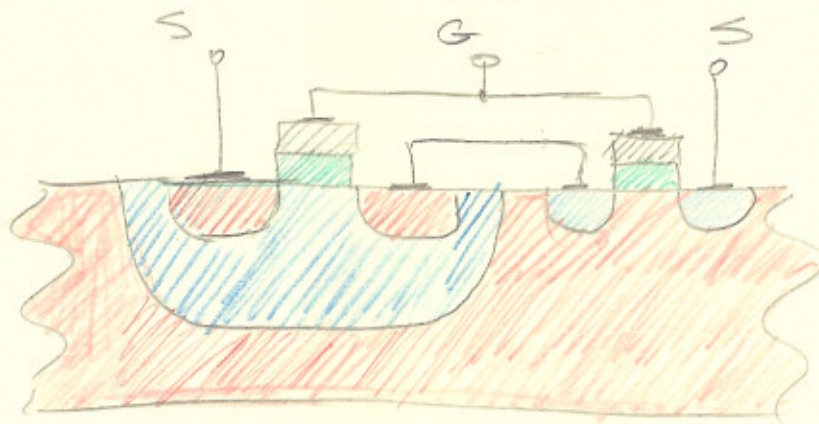
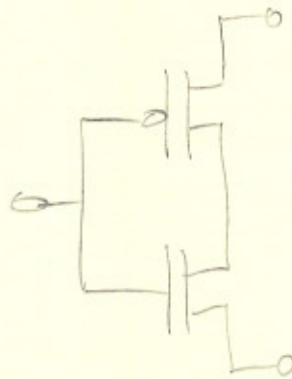
Recall

C-MOS



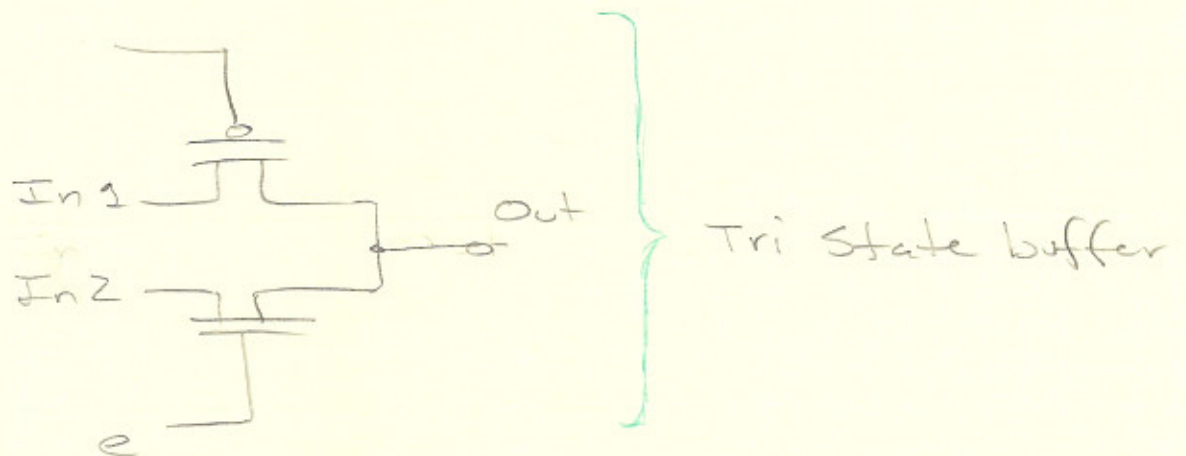
cmos cell

sr

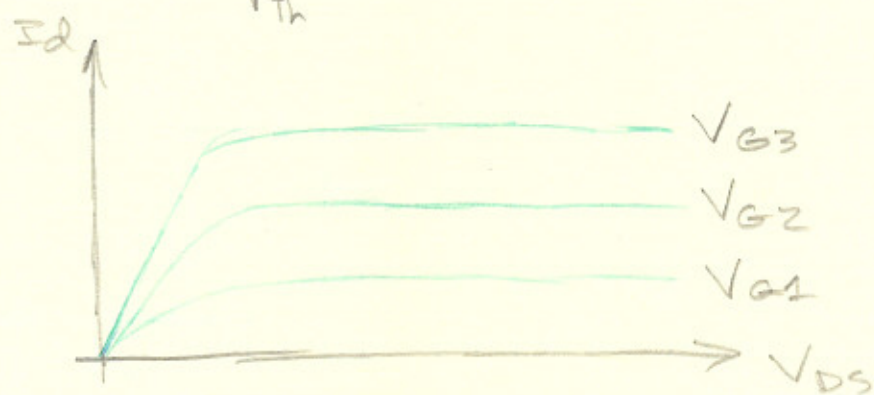
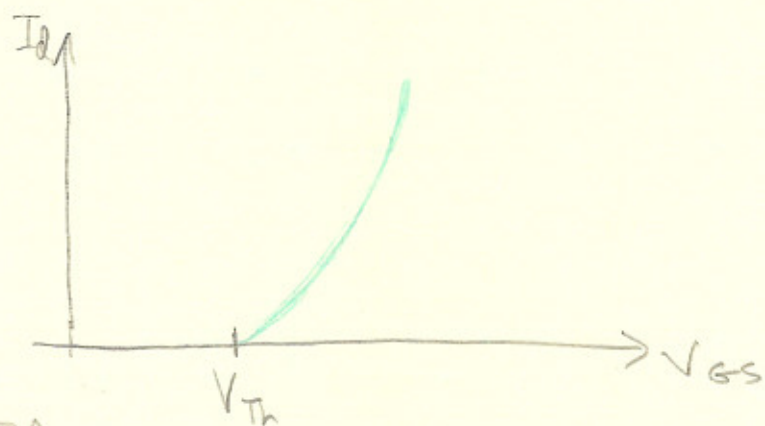


p channel
n channel

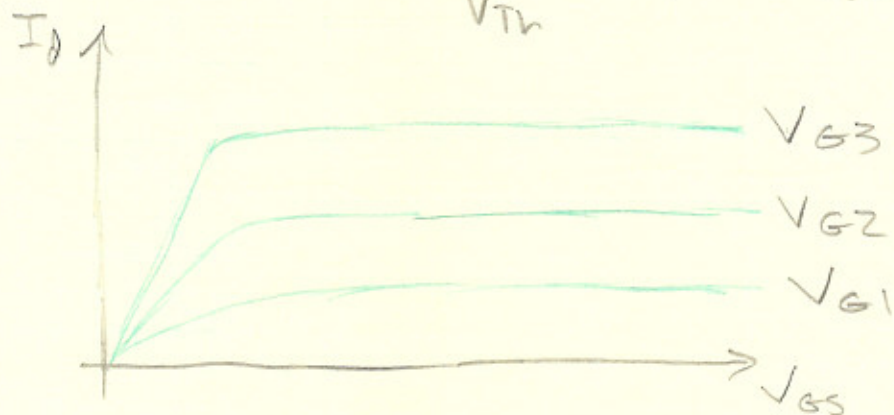
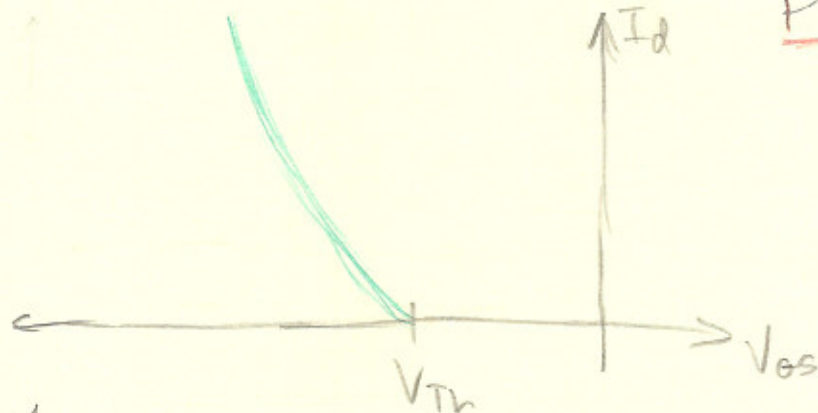
Unifying outputs using Cmos



if $e=1$ $Out = In2$
 $e=0$ $Out = Z$ (high impedance)

N channel

$$V_{G1} < V_{G2} < V_{G3}$$

P channel

$$V_{G1} < V_{G2} < V_{G3}$$